

JTAG MIRRORING CIRCUITRY AND METHODS

ABSTRACT OF THE DISCLOSURE

The present invention provides a JTAG mirroring circuit that augments the

5 functionality of a JTAG circuit in an existing circuit design. The JTAG circuits include a TAP controller, an instruction register, and a plurality of data registers. The JTAG mirroring circuit of the present invention augments the functionality of pre-existing JTAG circuitry by introducing additional data registers that are multiplexed with data registers in the pre-existing JTAG circuit. Each of the additional data registers may perform functions that are
10 not performed by data registers in the pre-existing JTAG circuit.

PA 3150249 v1

0908072476012504